

PHOTOCOUPLER PS9317L,PS9317L2

HIGH CMR, 10 Mbps, OPEN COLLECTOR OUTPUT TYPE, 8 mm CREEPAGE 6-PIN SDIP PHOTOCOUPLER -NEPOC Series-

DESCRIPTION

The PS9317L and PS9317L2 are optical coupled high-speed, active low type isolators containing a GaAlAs LED on the input side and a photodiode and a signal processing circuit on the output side on one chip.

The PS9317L and PS9317L2 are designed specifically for high common mode transient immunity (CMR) and low pulse width distortion.

The PS9317L is lead bending type (Gull-wing) for surface mounting.

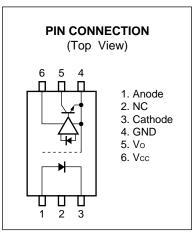
The PS9317L2 is lead bending type for long creepage distance (Gull-wing) for surface mount.

FEATURES

- Pulse width distortion (|tphl tplh| = 35 ns MAX.)
- High common mode transient immunity (CMH, CML = $\pm 15 \text{ kV}/\mu \text{s}$ MIN.)
- Half size of 8-pin DIP
- Long creepage distance (8 mm MIN. : PS9317L2)
- High-speed (10 Mbps)
- High isolation voltage (BV = 5 000 Vr.m.s.)
- Open collector output
- Pb-Free product
- <R> Safety standards
 - UL approved: File No. E72422
 - CSA approved: No. CA 101391
 - DIN EN60747-5-2 (VDE0884 Part2) approved: No. 40024069 (Option)

APPLICATIONS

- Measurement equipment
- PDP
- FA Network



TRUTH TABLE

LED	Output
ON	L
OFF	н

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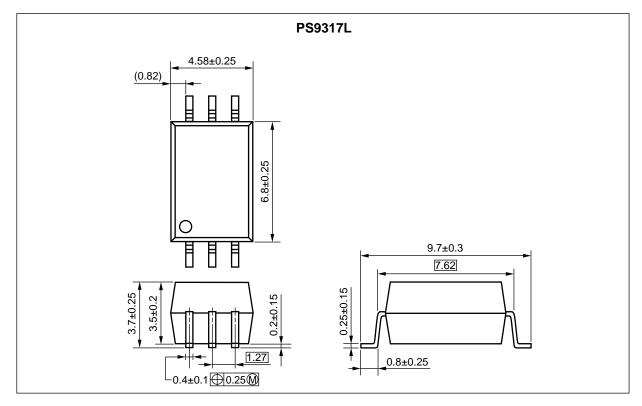
The mark <R> shows major revised points.

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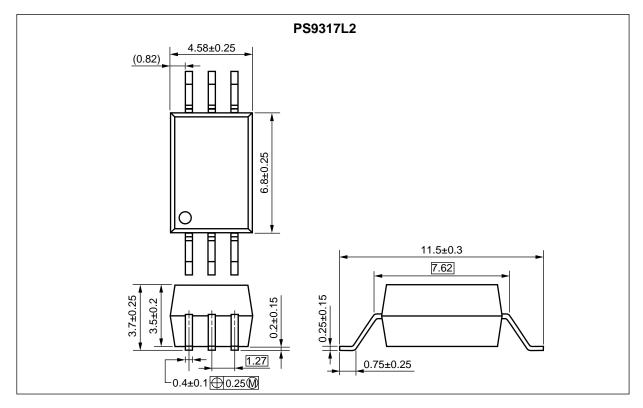
The revised points can be easily searched by copying an "<R>" in the PDF file and specifying it in the "Find what:" field.

PACKAGE DIMENSIONS (UNIT: mm)

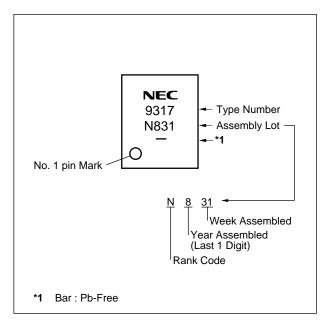
Lead Bending Type (Gull-wing) For Surface Mount



Lead Bending Type For Long Creepage Distance (Gull-wing) For Surface Mount



<R> MARKING EXAMPLE



PHOTOCOUPLER CONSTRUCTION

Parameter	PS9317L	PS9317L2	
Air Distance (MIN.)	7 mm	8 mm	
Outer Creepage Distance (MIN.)	7 mm	8 mm	
Isolation Distance (MIN.)	0.4 mm	0.4 mm	

<R> ORDERING INFORMATION

Part Number	Order Number	Solder Plating Specification	Packing Style	Safety Standard Approval	Application Part Number ^{*1}
PS9317L	PS9317L-AX	Pb-Free	20 pcs (Tape 20 pcs cut)	Standard products	PS9317L
PS9317L-E3	PS9317L-E3-AX	(Ni/Pd/Au)	Embossed Tape 2 000 pcs/reel	(UL, CSA approved)	
PS9317L2	PS9317L2-AX		20 pcs (Tape 20 pcs cut)		PS9317L2
PS9317L2-E3	PS9317L2-E3-AX		Embossed Tape 2 000 pcs/reel		
PS9317L-V	PS9317L-V-AX		20 pcs (Tape 20 pcs cut)	DIN EN60747-5-2	PS9317L
PS9317L-E3-V	PS9317L-E3-V-AX		Embossed Tape 2 000 pcs/reel	(VDE0884 Part2)	
PS9317L2-V	PS9317L2-V-AX		20 pcs (Tape 20 pcs cut)	Approved (Option)	PS9317L2
PS9317L2-E3-V	PS9317L2-E3-V-AX		Embossed Tape 2 000 pcs/reel		

*1 For the application of the Safety Standard, following part number should be used.

ABSOLUTE MAXIMUM RATINGS (TA = 25°C, unless otherwise specified)

Parameter		Symbol	Ratings	Unit
Diode	Forward Current ^{*1}	lF	30	mA
	Reverse Voltage	VR	5	V
Detector	Supply Voltage	Vcc	7	V
	Output Voltage	Vo	7	V
	Output Current	lo	25	mA
	Power Dissipation*2	Pc	40	mW
Isolation	Voltage ^{*3}	BV	5 000	Vr.m.s.
Operating Ambient Temperature		TA	-40 to +85	°C
Storage Temperature		Tstg	-55 to +125	°C

*1 Reduced to 0.3 mA/°C at $T_A = 25$ °C or more.

- *2 Applies to output pin Vo (collector pin). Reduced to 1.5 mW/°C at TA = 65°C or more.
- *3 AC voltage for 1 minute at $T_A = 25^{\circ}$ C, RH = 60% between input and output. Pins 1-3 shorted together, 4-6 shorted together.

RECOMMENDED OPERATING CONDITIONS

Parameter	Symbol	MIN.	TYP.	MAX.	Unit
Low Level Input Voltage	VFL	0		0.8	V
High Level Input Current	Ifh	6		12	mA
Supply Voltage	Vcc	4.5	5.0	5.5	V
TTL (R∟ = 1 kΩ, loads)	Ν			5	
Pull-up Resistor	R∟	330		4 k	Ω

Parameter		Symbol	Conditions	MIN.	TYP. ^{*1}	MAX.	Unit
Diode	de Forward Voltage V_F $I_F = 10 \text{ mA}, T_A = 25^{\circ}\text{C}$		I⊧ = 10 mA, T₄ = 25°C	1.2	1.56	1.9	V
	Reverse Current	Ir	Vr = 3 V, Ta = 25°C			10	μA
	Terminal Capacitance	Ct	V = 0 V, f = 1 MHz, T _A = 25°C		60		pF
Detector	High Level Output Current	Іон	Vcc = Vo = 5.5 V, VF = 0.8 V		1	100	μA
	Low Level Output Voltage ^{*2}	Vol	Vcc = 5.5 V, I⊧ = 5 mA, Io∟ = 13 mA		0.2	0.6	V
	High Level Supply Current	Іссн	Vcc = 5.5 V, IF = 0 mA, Vo = open		4	7	mA
	Low Level Supply Current	Iccl	Vcc = 5.5 V, I _F = 10 mA, Vo = open		6	10	mA
Coupled	Threshold Input Current	IFHL	Vcc = 5 V, Vo = 0.8 V, RL = 350 Ω		2.5	5	mA
	$(H \rightarrow L)$						
	Isolation Resistance	R⊦o	$V_{I-0} = 1 \text{ kV}_{DC}, \text{ RH} = 40 \text{ to } 60\%,$ $T_A = 25^{\circ}\text{C}$	10 ¹¹			Ω
	Isolation Capacitance	CI-0	V = 0 V, f = 1 MHz, T _A = 25°C		0.7		pF
	Propagation Delay Time	t PHL	T _A = 25°C		40	75	ns
	$(H \rightarrow L)$					100	
	Propagation Delay Time	t PLH	T _A = 25°C		35	75	
	$(L \rightarrow H)$					100	
	Rise Time	tr	Vcc = 5 V, R∟ = 350 Ω, C∟ = 15 pF,		20		
	Fall Time	tr	IF = 7.5 mA, VTHHL = VTHLH = 1.5 V		5		
	Pulse Width Distortion (PWD)	tphl—tplh			5	35	
	Propagation Delay Skew	tрsк				40	
	Common Mode Transient Immunity at High Level Output	СМн	$\label{eq:Vcc} \begin{array}{l} V_{CC} = 5 \ V, \ R_L = 350 \ \Omega, \ T_A = 25^\circ C, \\ I_F = 0 \ mA, \ V_O > 2 \ V, \ V_{CM} = 1.5 \ kV \end{array}$	15			kV/µs
	Common Mode Transient Immunity at Low Level Output	CM∟	$\label{eq:Vcc} \begin{array}{l} V_{CC} = 5 \ V, \ R_L = 350 \ \Omega, \ T_A = 25^\circ C, \\ I_F = 7.5 \ mA, \ V_O < 0.8 \ V, \ V_{CM} = 1.5 \ kV \end{array}$	15			kV/µs

ELECTRICAL CHARACTERISTICS (T_A = -40 to +85°C, unless otherwise specified)

*1 Typical values at $T_A = 25^{\circ}C$.

*2 Because V_{OL} of 2 V or more may be output when LED current input and when output supply of V_{CC} = 2.6 V or less, it is important to confirm the characteristics (operation with the power supply on and off) during design, before using this device.

USAGE CAUTIONS

- 1. This product is weak for static electricity by designed with high-speed integrated circuit so protect against static electricity when handling.
- 2. By-pass capacitor of 0.1 μ F is used between Vcc and GND near device. Also, ensure that the distance between the leads of the photocoupler and capacitor is no more than 10 mm.
- 3. Avoid storage at a high temperature and high humidity.

NOTES ON HANDLING

Cautions regarding noise

Be aware that when voltage is applied suddenly between the photocoupler's input and output or between collector-emitters at startup, the output transistor may enter the on state, even if the voltage is within the absolute maximum ratings.

<R> SPECIFICATION OF VDE MARKS LICENSE DOCUMENT

Parameter		Symbol	Speck	Unit
Application classification (DIN EN 60664-1 VDE0110 for rated line voltages \leq 300 Vr.m.s. for rated line voltages \leq 600 Vr.m.s.		IV III		
Climatic test class (DIN EN 60664-1 VDE0110)			40/85/21	
Dielectric strength maximum operating isolation voltage Test voltage (partial discharge test, procedure a for type test and random test) $U_{pr} = 1.5 \times U_{IORM}$, Pd < 5 pC		Uiorm Upr	1 130 1 695	V _{peak} V _{peak}
Test voltage (partial discharge test, procedure b for a U_{Pr} = 1.875 \times U_{\text{IORM}}, P_{d} $< 5~pC$	all devices)	Upr	2 119	V _{peak}
Highest permissible overvoltage		Utr	8 000	Vpeak
Degree of pollution (DIN EN 60664-1 VDE0110 Part	: 1)		2	
Clearance distance	PS9317L		>7.0	mm
	PS9317L2		>8.0	
Creepage distance	PS9317L		>7.0	mm
	PS9317L2		>8.0	
Comparative tracking index (DIN IEC 112/VDE 0303 Part 1)		СТІ	175	
Material group (DIN EN 60664-1 VDE0110 Part 1)			III a	
Storage temperature range		Tstg	-55 to +125	°C
Operating temperature range		TA	-40 to +85	°C
Isolation resistance, minimum value $V_{IO} = 500 \text{ V dc at } T_A = 25^{\circ}\text{C}$ $V_{IO} = 500 \text{ V dc at } T_A MAX. at least 100^{\circ}\text{C}$		Ris MIN. Ris MIN.	10 ¹² 10 ¹¹	Ω Ω
Safety maximum ratings (maximum permissible in case of fault, see thermal derating curve) Package temperature Current (input current IF, Psi = 0) Power (output or total power dissipation)		Tsi Isi Psi	175 400 700	°C mA mW
Isolation resistance V _{IO} = 500 V dc at T _A = Tsi		Ris MIN.	10 ⁹	Ω

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	• Follow related laws and ordinances when disposing of the product. If there are no applicable laws and/or ordinances, dispose of the product as recommended below.
	 Commission a disposal company able to (with a license to) collect, transport and dispose of materials that contain arsenic and other such industrial waste materials.
	2. Exclude the product from general industrial waste and household garbage, and ensure that the product is controlled (as industrial waste subject to special control) up until final disposal.
	• Do not burn, destroy, cut, crush, or chemically dissolve the product.
	• Do not lick the product or i any way allow it to enter the mouth.